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Improved Subset Generation For The MU-Decoder

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IMPROVED SUBSET GENERATION FOR THE MU-DECODER

A Thesis

Submitted to the Graduate Faculty of the
Louisiana State University and
Agricultural and Mechanical College
in partial fulfillment of the
requirements for the degree of
Master of Science in Electrical Engineering

in

Electrical Engineering

by

Utsav Agarwal
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# Contents

Acknowledgments .................................................. ii

List of Tables ......................................................... iv

List of Figures ......................................................... v

Abstract ............................................................... vi

Chapter

1 Introduction ......................................................... 1
   1.1 Problem Definition ............................................. 3

2 MU-Decoder .......................................................... 8
   2.1 MU-Decoder Structure and Ordered Partitions ................. 9
   2.2 Properties of MU-Decoder ...................................... 11

3 Totally-Ordered Sets .............................................. 13
   3.1 Totally-Ordered Sets in the Boolean Lattice ................. 13
   3.2 Totally-Ordered Source and Output Sets ..................... 15
   3.3 Canonical Form of Source Set .................................. 17

4 Generating a Given Totally Ordered Set ......................... 20
   4.1 Generating a Large Totally-Ordered Set ...................... 20
   4.2 Generating a Set of Non-isomorphic Totally Ordered Sets .... 22

5 Hardware Enhancement for Partition Generation ............... 23
   5.1 Ordered Partition Translation .................................. 24

6 Generic Subsets ..................................................... 38
   6.1 Traversing the Boolean Lattice ................................ 38
       6.1.1 Single Total Order 8 ..................................... 38

7 Conclusion .......................................................... 44
   7.1 Work Covered ................................................... 44
   7.2 Future Work .................................................... 45

Bibliography .......................................................... 47

Vita ................................................................. 50
## List of Tables

1.1 Results in this thesis ................................................................. 7

5.1 Block Number of $a \in B^0_m$ over translation .............................. 33
# List of Figures

1.1  An Illustration of Frame Granularity ................................................. 2
1.2  Boolean Lattice .................................................................................. 4
1.3  Totally-Ordered Sets ................................................................. 5
1.4  Totally Ordered Subset Restriction ............................................. 6
2.1  $x$-to-$n$ MU-Decoder MD($x, z, y, n$) ........................................ 9
3.1  Boolean Lattice $G_4$ ................................................................. 14
5.1  Current Selector Module ............................................................ 23
5.2  $C$-uniform translation ............................................................... 33
5.3  Address Generator ................................................................. 36
5.4  Selector Module Hardware Structure ........................................ 37
6.1  $2d$ spaced totally-ordered subsets ........................................ 39
6.2  $XY$ Totally-Ordered subsets .................................................. 43
7.1  Multiple Totally-Ordered paths produced from one Totally-Ordered set of subsets ....................................................... 46
Abstract

The MU-Decoder is a hardware subset generator that finds use in partial reconfiguration of FPGAs and in numerous other applications. It is capable of generating a set $S$ of subsets of a large set $\mathbb{Z}_n$ with $n$ elements. If the subsets in $S$ satisfy the “isomorphic totally-ordered property,” then the MU-Decoder works very efficiently to produce a set of $u$ subsets in $O(\log n)$ time and $\Theta(n\sqrt{u} \log n)$ gate cost. In contrast, a naive approach requires $\Theta(un)$ gate cost. We show that this low cost for the MU-Decoder can be achieved without the isomorphism constraint, thereby allowing $S$ to include a much wider range of subsets. We also show that if additional constraints on the relative sizes of the subsets in $S$ can be placed, then $u$ subsets can be generated with $\Theta(n\sqrt{u})$ cost. This uses a new hardware enhancement proposed in this thesis. Finally, we show that by properly selecting $S$ and by using some elements of traditional methods, a set of $u\binom{n}{d}$ subsets can be produced with $\Theta(n\sqrt{u} \log u)$ cost.
Chapter 1
Introduction

Many modern applications utilize Field Programmable Gate Arrays (FPGAs) [1, 35], which includes intelligent systems [3, 23, 24], scientific applications [8, 13], defense and aerospace systems [19, 25, 27, 34], communication and signal processing [6, 14, 22, 26, 32], instrumentation [10, 30], finance [33].

Partial Reconfiguration [4, 5, 7, 9, 18, 20, 31] (PR) is an important feature of reconfigurable computing that allows a portion of the configuration fabric to be reconfigured at runtime. The feature allows for an efficient use of the chip’s real estate. However PR needs to be quick (to be useful at real time). The unit of reconfiguration is typically called a “frame.” A frame may contain hundreds or thousands of reconfigurable bits (the entire chip could contain millions). If frames are too large (and granular), a larger portion of the configurable fabric may have to be reconfigured.

Example 1.1. We consider the illustrative example taken from Ashrafi [2] and Jordan [17]. Figure 1.1 shows us how frame granularity affects the partial configuration of a chip. The blue colored regions represent the area that needs to be reconfigured and the yellow colored regions represent the extra part of the frame that needs to be reconfigured as well during partial configuration.

This points toward using small frames. However, to configure a frame, it must first be selected and then a data path established to it, before the configuration bits can be input to the frame. Conventional ways to select a frame come down to the use of a 1-hot decoder, that selects one frame at a time (or a 1-element subset of the set of all frames). If we use the illustration of Example 1.1 to make frames small, then there will be many frames to reconfigure and many iterations of using the 1-hot decoder; essentially a time-consuming exercise. The MU-Decoder [16] shows how a subset (of more than 1 frame) can be selected. The scan-path architecture [2] shows how the configuration bits can be delivered to the
Figure 1.1: An Illustration of Frame Granularity. (a) shows the location of the part needing reconfiguration. It occupies only 15% of the total area. Part (b)-(e) show the impact of various frame size and shapes. For example, in part (c), 6 of the 8 “tall” frames are needed to cover the blue area. So while the partial reconfiguration (PR) area is 15%, the configuration area is 6/8 or 75%. The square frames in part(d) fare worse, requiring a total reconfiguration. With smaller frames, the configuration area reduces.
multiple selected frames. The MU-Decoder is, however, efficient only for certain types of subsets. In this thesis, we extend the range of subsets for which the MU-Decoder works. We also construct a framework for generating arbitrary subsets on the MU-Decoder.

The core problem addressed is that of generating a subset. While this is useful in FPGA partial reconfiguration, the application of subset generation is much wider, including wireless and heterogeneous networks [11], vehicle control units [31], database servers [5], image compression [29], bioinformatics [15] and much more.

1.1 Problem Definition

Let $\mathbb{Z}_n = \{0, 1, \cdots, n - 1\}$. The goal is to generate a set $\mathcal{S} = \{S_i : 0 \leq i < u, S_i \subseteq \mathbb{Z}_n\}$. In this thesis, we focus on a “totally-ordered” set $\mathcal{S}$ where $S_0 \subset S_1 \subset \cdots \subset S_{u-1}$. The $2^n$ subsets of $\mathbb{Z}_n$ can be viewed on a Boolean Lattice (Haase diagram) [28] as shown in Figure 1.2(b) (an experiment example where $n = 4$ is in Figure 3.1 on page 14). In this lattice a totally-ordered set is a set of points on a path from $\emptyset$ to $\mathbb{Z}_n$ as shown in Figure 1.2(b) with $S_0, S_1, \cdots, S_{w-1} \in \mathcal{S}$, which is totally-ordered.

It has been shown [17] that $\mathcal{S}$ can be produced using an MU-Decoder with a delay of $O(\log n)$ and gate-cost of $O(n\sqrt{w\log u})$. Then Jordan and Vaidyanathan [16] extend this to “isomorphic” totally-ordered sets (see Figure 1.3(a)) $S_0, S_1, \cdots, S_{v-1}$ with $S_m = \{S_i^m : 0 \leq i < w$ and $S_i^m \subseteq \mathbb{Z}_n\}$.

Here combinations of the subsets of any two $S_m, S_{m'}$ are in one-to-one correspondence. That is, if $S_m$ contains subsets of size $a_0, a_1, \cdots, a_{w-1}$ then so does $S_{m'}$. The cost of producing $S_0, S_1, \cdots, S_{v-1}$ is $O((v + w)n\log w)$. If the $uw$ subsets were to be produced naively by a simple $(uw) \times n$ look-up table (LUT) its cost would be $O(uwn)$. So the cost of producing the subsets on the MU-decoder is substantially smaller than that of a brute force “LUT Decoder” [17] method. While the LUT-Decoder can produce arbitrary subsets expensively, the MU-Decoder can produce only a certain type of subsets (for example, isomorphic totally-ordered subsets) inexpensively. In a way, the work of this thesis is to extend the type of subsets that the MU-Decoder can produce effectively. We show that the set $S_0, S_1, \cdots, S_{v-1}$ can be produced on an MU-Decoder with $O((v + w)n\log(uw))$ (same as
before) but without the restriction of isomorphism on the totally-ordered sets \( S_0, S_1, \ldots, S_{w-1} \) (see Figure 1.3(b)). To implement the subset of the type in Figure 1.3(b), the method of Jordan and Vaidyanathan we will need a MU-Decoder of cost \( O(vwn \log(uv)) \). Considering that \( v \) and \( w \) could be quite large (in hundreds) the difference in cost could be significant (a factor of tens). Next, we develop properties of totally-ordered sets that allow us to increase the efficiency of the MU-Decoder. For the set \( S_0, S_1, \ldots, S_{v-1} \), each with \( w \) subsets, the cost can be reduced further to \( \Theta(n(v + w)) \). This is done through a hardware enhancement of the MU-Decoder that does not change its gate-cost significantly. However, it adds some additional conditions on the sets \( S_0, S_1, \ldots, S_{v-1} \). These conditions, while not as strict as isomorphism, are stricter than just total order. They put constraints on the gaps in the path representing totally-ordered sets (Figure 1.4). The shaded circles in the figure represent the original (generator) subsets of each totally-ordered set and the unshaded circles represent new subsets generated from the generator subsets in the MU-Decoder. Now the restriction

Figure 1.2: Boolean Lattice
The subsets generated have the condition of being equally spaced in the Boolean Lattice (of equal Hamming Distance [12]) from their corresponding generator subset. The above approach allows us to pick the “best” sets of subsets, $S_0, S_1, \cdots, S_{v-1}$ such that they are strategically placed close (on the Boolean Lattice) to the subsets that we wish to generate. Together with a 1-hot Decoder, we can now generate $\binom{n}{d} z^2 \log z$ subsets in $O(d \log n)$ time using a MU-Decoder of gate cost $O(zn \log z)$ and delay $O(\log n)$. This is a substantial expansion of the MU-Decoder range for efficient operation. In fact, the method used here can also be ported to the LUT Decoder. However, the cost of the LUT decoder will still be $O(z^2 n \log z)$.

Chapter 2 presents the preliminary concepts used throughout the thesis.

In Chapter 3, we find ourselves in the midst of defining the conditions for sets being totally-ordered.
In Chapter 4, we prove that isomorphism is not required to produce disjoint output sets which are individually totally-ordered.

In Chapter 5, we discuss hardware enhancements for the MU-Decoder, which increase its productivity by accommodating the concept of translation (introduced in this thesis) into the hardware.

In Chapter 6, we use a generic set of subsets and cover an additional $d$ distance on the Boolean Lattice to produce an $\binom{n}{d}$ factor increase in subsets in $O(d \log n)$ time.

Finally, in Chapter 7, we summarize our findings and discuss the future scope of this research and ideas that can be further developed upon.
<table>
<thead>
<tr>
<th>#</th>
<th>MU-Decoder configuration</th>
<th>Constraint</th>
<th>Cost</th>
<th>Subsets</th>
<th>Equivalent LUT cost</th>
<th>Ref.</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>( MD(x, y, z, n) )</td>
<td>none</td>
<td>( \Theta(2^x(x + z) + n \log z(2^y + z)) )</td>
<td>( n \min{2^x, 2^y \lfloor \log z \rfloor } )</td>
<td></td>
<td>[17]</td>
</tr>
<tr>
<td>2</td>
<td>( MD([\log(z - 1)], [\log z], z, n) )</td>
<td>Totally-ordered, Isomorphic</td>
<td>( \Theta(zn \log z) )</td>
<td>( z^2 )</td>
<td>( nz^2 )</td>
<td>[16]</td>
</tr>
<tr>
<td>3</td>
<td>( MD([\log(z - 1)], [\log z], z, n) )</td>
<td>Non-Isomorphic</td>
<td>( \Theta(zn \log z) )</td>
<td>( z^2 )</td>
<td>( nz^2 )</td>
<td>[this thesis]</td>
</tr>
<tr>
<td>4</td>
<td>( MD^*([\log(z - 1)], [\log(z \log z)], z, n) )</td>
<td>Translated Partitions</td>
<td>( O(zn \log z) )</td>
<td>( z^2 \log z )</td>
<td>( nz^2 \log z )</td>
<td>[this thesis]</td>
</tr>
<tr>
<td>5</td>
<td>( MD^*([\log(z - 1)], [\log(z \log z)], z, n) )</td>
<td>Generic Subsets+</td>
<td>( O(zn \log z) )</td>
<td>( z^2 \log z ( \binom{n}{d} ) )</td>
<td>( nz^2 \log z ( \binom{n}{d} ) )</td>
<td>[this thesis]</td>
</tr>
</tbody>
</table>

* This MU-Decoder uses a hardware enhancement proposed in this thesis.
+ Subsets with Hamming distance \( d \) from total order with \( O(d \log n) \) delay

Table 1.1: Results in this thesis
Chapter 2
MU-Decoder

In this chapter, we describe the MU-Decoder and some of its relevant properties. We start the chapter by defining the standard form of a subset when represented as a string of bits, also called the characteristic representation of a subset. We start the first section with a description of the MU-Decoder, along with a diagram and all the terms associated with the same as described by Jordan and Vaidyanathan [16]. We define input and output words and the selector address along with the fact that we use ordered partition in this thesis and not regular partitions to represent the selector module. We describe the casting of a source word into an ordered partition, to produce the output word with the help of an indicator set. We show how we group these words into sets of subsets, moreover even after grouping how the casting of source words to set of ordered partitions still holds, and support it with an example.

We move on to the next section to describe the properties of the MU-Decoder. Since all these properties are discussed in the earlier work, we define them and then cite each of them for proper referencing. In this section, we discuss the gate cost, time delay and subsets produced by an MU-Decoder. We move on to define totally-ordered sets along with the property of isomorphism. Before we proceed we define the characteristic representation of a set.

Definition 2.1. Let \( \mathbb{Z}_v = \{0, 1, \ldots, v - 1\} \) be a \( v \)-element set. Every subset \( S \subseteq \mathbb{Z}_v \) can be represented in hardware as a binary string \( \langle s_0, s_1, \ldots, s_{v-1}\rangle \) where \( s_i = 1 \) if and only if \( i \in S \). This representation of a subset is called a characteristic representation of a subset. We will, in general, not distinguish a subset from its characteristic representation. Conversely, every \( v \)-bit binary string represents a subset of \( \mathbb{Z}_v \).

In this thesis, we will use subsets of different \( \mathbb{Z}_v \)'s and sets of subsets of these \( \mathbb{Z}_v \)'s. In general we will use the term "subset" to refer to \( S \subseteq \mathbb{Z}_v \) and the term "set" to refer to
\[ S = \{ S_0, S_1, \cdots \}, \text{ where } S_i \in \mathbb{Z}_v, \text{ or } S \in \mathcal{P}(\mathbb{Z}_v). \]

### 2.1 MU-Decoder Structure and Ordered Partitions

The MU-Decoder was proposed by Jordan and Vaidyanathan [16] [17]. It allows for efficient generation of multiple subsets of \( \mathbb{Z}_n \). Figure 2.1 shows the general structure of an MU-Decoder. For any \( x, z, y, n \), where \( x, y << z << n \), an MU-Decoder, \( \text{MD}(x, z, y, n) \) has \( x + y \) external input bits, \( z \) internal signals and \( n \) output bits. Functionally, the \( x \)-bit input word selects one of \( 2^x = X \) locations in the LUT and outputs the corresponding \( z \)-bit source word. This source word and a separate \( y \)-bit selector address are input to the Mapping Unit where they are converted into an \( n \)-bit output word representing a subset \( S \subseteq \mathbb{Z}_n \).

![Figure 2.1: x-to-n MU-Decoder MD(x, z, y, n)](image-url)
Mapping Unit multicasts the \( z \)-bit source word to the various output word bits based on an \textit{ordered partition} selected by the \( y \)-bit selector address. Before we proceed it is important to understand the role of the source word and an ordered partition to generate an output word.

\textbf{Definition 2.2.} An ordered \( z \)-partition \( \pi \) of \( \mathbb{Z}_n \) is a list \( \langle B_0, B_1, \cdots, B_{z-1} \rangle \) pairwise disjoint subsets of \( \mathbb{Z}_n \), that cover \( \mathbb{Z}_n \). That is for \( 0 \leq i < j < z \), \( B_i \subseteq \mathbb{Z}_n \), \( B_i \cap B_j = \emptyset \) and \( \bigcup_{i=0}^{z-1} B_i = \mathbb{Z}_n \) \( \square \)

The difference between an ordered partition and a conventional partition is (a) the blocks \( B_i \) of an ordered partition can be empty and (b) the blocks are ordered. In this thesis, all ordered partitions have \( z \)-blocks (where \( z \) is the source word size). We will use the term ordered partition to mean an ordered \( z \)-partition. Let \( L = \langle L(i) : 0 \leq i < z \rangle \) be a source word and let \( \pi = \langle B_i : 0 \leq i < z \rangle \) be an ordered partition.

\textbf{Definition 2.3.} For any Boolean variable (condition) \( \nu \) any set \( S \) defines the indicator set:

\[
[\mathbbm{1}(S, \nu)] = \begin{cases} 
S, & \text{if } \nu = 1 \\
\emptyset, & \text{if } \nu = 0 
\end{cases}
\]

\( \square \)

We are now in a position to define how a source word combines with an ordered partition to produce subset \( S \).

\textbf{Definition 2.4.} Define a cast of \( L \) into \( \pi \) (denoted by \( L \circ \pi \)) as a subset \( S = \bigcup_{i=0}^{z-1} [\mathbbm{1}(B_i, L(i))] \) (where \( S \subseteq \mathbb{Z}_n \)). It is easy to say that for all \( a \in \mathbb{Z}_n \), \( a \in S \) if and only if there exists \( i \) such that \( a \in B_i \) and \( L(i) = 1 \). \( \square \)

In the definition above, we remind the reader that there always exists a unique block \( B_i \) to which \( a \) belongs. The question is simply that of the block number and the corresponding source word bit.
Example 2.1. For \( n = 8 \), let source words \( L_0 = \langle 0111 \rangle \) and \( L_1 = \langle 0011 \rangle \), and let ordered partitions \( \pi_0 = \langle \{0,2,4,6\}, \{1,5\}, \{3\}, \{7\} \rangle \) and \( \pi_1 = \langle \{0,1,2,3\}, \{4,5\}, \{6\}, \{7\} \rangle \). Then from definition 2.4 we have \( L_0 \circ \pi_0 = S_0^0 = \bigcup_{i=0}^{x-1} [1(B_i^0, L_0(i))] = [1(B_0^0, L_0(0))] \cup [1(B_1^0, L_0(1))] \cup [1(B_2^0, L_0(2))] \cup [1(B_3^0, L_0(3))] = \emptyset \cup B_0^0 \cup B_2^0 \cup B_3^0 = \langle 0,1,0,1,0,1,0,1 \rangle \). Similarly we get \( L_1 \circ \pi_0 = S_1^0 = \langle 0,0,0,1,0,0,1,0,1 \rangle \), \( L_0 \circ \pi_1 = S_0^1 = \langle 0,0,0,0,1,1,1,1,1 \rangle \) and \( L_1 \circ \pi_1 = S_1^1 = \langle 0,0,0,0,0,1,1,1 \rangle \).

In general the LUT of Figure 2.1 contains the \( X = 2^x \) source words \( L_0, L_1, \ldots L_{X-1} \). The source set is \( \mathcal{L} = \{ L_i : 0 \leq i < X \} \). Similarly the selector module contains the representation of at least \( 2^y = Y \) separate ordered partitions. Let \( \Pi = \{ \pi_j : 0 \leq j < Y \} \) denote the set of ordered partitions. Then we will use notation \( \mathcal{L} \circ \pi = \mathcal{S} \) and \( \mathcal{L} \circ \Pi = \bigcup_{j=0}^{Y-1} \mathcal{L} \circ \pi_j = \mathcal{S}_j = \mathcal{S} \) to indicate set of subsets of \( \mathbb{Z}_n \).

For example 2.1 we can say \( \mathcal{L} = \{ L_0, L_1 \} \) and \( \Pi = \{ \pi_0, \pi_1 \} \), hence the set of subsets \( \mathcal{S}_0 = \{ S_0^0, S_1^0 \} = \mathcal{L} \circ \pi_0 \), \( \mathcal{S}_1 = \{ S_0^1, S_1^1 \} = \mathcal{L} \circ \pi_1 \). Hence \( \mathcal{S} = \mathcal{S}_0 \cup \mathcal{S}_1 = \mathcal{L} \circ \Pi \)

Observe that if the characteristic representation of \( \mathcal{S} = \langle S(a) : 0 \leq a < n \rangle \) then \( S(a) = 1 \) if and only if \( a \in B_i \) and \( L(i) = 1 \). To implement this consider a hardwiring of the blocks of \( \pi \) as shown in Figure 2.1, then a cast of \( L \) into \( \pi \) is simply multicasting \( L \) through this hardwiring. The Mapping Unit implements this multicast by configuring through the selector word each of its \( n \) \( z \)-to-1 MUXes. It has been shown in theorem 2.1 that \( \text{MD}(x, z, y, n) \) has a cost and delay.

2.2 Properties of MU-Decoder

In this section, we list some relevant properties of the MU-Decoder. These are all from Jordan and Vaidyanathan [16].

Theorem 2.1. An MU-Decoder \( \text{MD}(x, z, y, n) \) as shown in Figure 2.1 has

Gate Cost=\( O(2^x(x+z)+n \log z(z+2^y)) \), Delay=\( O(x+z+y+\log n) \)

producing min\( \{2^x, 2^y \log z\} \) independent subsets or as many as \( O(2^{x+y}) \) subsets. \( \square \)

Now we define a totally-ordered set of subsets.
**Definition 2.5.** A set $\mathcal{S} = \{ S_i : 0 \leq i < u \}$ is totally ordered if and only if there exists an ordering (permutation) of the indices of $\mathbb{Z}_n$ such that for all $0 \leq i < u - 1$, $S_{f(i)} \subset S_{f(i+1)}$.

Later in Section 3.3 (see page 17) we show that we can assume without loss of generality that $f(i) = i$; hence $S_0 \subset S_1 \subset \cdots S_{n-1}$ as stated by Jordan and Vaidyanathan [16].

**Definition 2.6.** Let $S_0, S_1, \cdots, S_{v-1}$ be totally-ordered sets. These form an isomorphic set of totally-ordered sets if and only if these subset’s cardinalities are in one-to-one correspondence.

That is if $S_0$ has elements of cardinality $a_0 < a_1 < \cdots < a_{n-1}$ then so do all $S_i$’s.

Figure 1.3(a) (see page 5), illustrates an isomorphic totally-ordered set. We note that each line represents a totally-ordered set, and these lines contain 4 subsets each. A subset on one totally-ordered set shares the level with a subset from each of the other totally-ordered sets. From Figure 1.2(a) we know that at level $\ell$ all subsets are of size $\ell$. Hence the subsets are isomorphic. This theorem has been proved by Jordan and Vaidyanathan [16].
Chapter 3
Totally-Ordered Sets

In this chapter, we discuss the properties of “totally-ordered sets” as they relate to the MU-Decoder. Jordan and Vaidyanathan [16] showed that totally-ordered sets have an efficient implementation on the MU-Decoder. In this thesis, we expand the scope of this observation by extending the range of sets for which the MU-Decoder is efficient. In this chapter, we formally define totally-ordered sets and derive some properties of the source set (see definition 2.5 on page 12) needed for producing totally-ordered sets. We end the chapter with the definition of a “canonical form” of source words (section 3.3). Then we justify one assumption that the source words are represented in canonical form as just a convenience for studying totally-ordered sets.

We begin with a definition of a totally-ordered set (of subsets of $\mathbb{Z}_n$), taken from Jordan and Vaidyanathan [16].

Definition 3.1. A set $S = \{S_0, S_1, \cdots, S_{u-1}\} \subseteq \mathcal{P}(\mathbb{Z}_n)$ is totally-ordered if and only if there exists an ordering of elements of $S$, such that $S_0 \subset S_1 \subset \cdots \subset S_{u-1}$.

Example 3.1. Let $n = 8, u = 5$. Then with $S_0 = \{0, 6\}$, $S_1 = \{0, 1, 6\}$, $S_2 = \{0, 1, 5, 6\}$, $S_3 = \{0, 1, 5, 6, 7\}$ and $S_4 = \{0, 1, 2, 5, 6, 7\}$, the set $S = \{S_0, S_1, \cdots, S_4\} \subseteq \mathcal{P}(8)$ is totally-ordered, with $S_0 \subset S_1 \subset S_2 \subset S_3 \subset S_4$. In terms of the characteristic string of a set we have $S_0 = \langle 10000010 \rangle$, $S_1 = \langle 11000010 \rangle$, $S_2 = \langle 11000110 \rangle$, $S_3 = \langle 11000111 \rangle$ and $S_4 = \langle 11100111 \rangle$. Recall that $\vec{S}$ is a characteristic string of set $S$.

3.1 Totally-Ordered Sets in the Boolean Lattice

For a $n$ element set $\mathbb{Z}_n$, its power set $\mathcal{P}(\mathbb{Z}_n)$ (with $2^n$ elements) can be represented as a boolean lattice [28]. The most common expression of this lattice is as a Hasse diagram [28]. This is a graph $\mathcal{G}_n$ with $2^n$ nodes (one per element of $\mathcal{P}(\mathbb{Z}_n)$). Two nodes $S_i, S_j$ with characteristic strings $\sigma_i, \sigma_j$ have an edge between them if and only if $\sigma_i, \sigma_j$ differ in exactly one bit, that is $S_i, S_j$ are different by exactly one element of $\mathbb{Z}_n$. Typically nodes of $\mathcal{G}_n$ are
arranged in $n + 1$ levels (numbered $0, 1, \cdots, n$) such that level $\ell$ contains $\binom{n}{\ell}$ nodes (subsets of $\mathbb{Z}_n$) with $\ell$ elements. Therefore, the empty set $\emptyset$ is the only one at level 0 and the set $\mathbb{Z}_n$ is the only one at level $n$. Figure 3.1 shows the representation of Boolean Lattice $G_4$ with $2^4$ nodes and 5 levels. We remember from Figure 1.2(a) (see page 4) that $\ell$ holds $\binom{n}{\ell}$ subsets of size $\ell$. Observe that if there is an edge in $G_n$ between $S_i$ and $S_j$ then they must be in adjacent levels, say $\ell$ and $\ell + 1$. Without loss of generality let $S_i$ have $\ell$ elements and $S_j$ have $\ell + 1$ elements. Then $S_j = S_i \cup \{a\}$, where $a$ the only element of $S_j$ that is not in $S_i$. Therefore, $S_i \subset S_j$. We view $G_n$ as a directed (acyclic) graph where each undirected edge between $S_i$ and $S_j$ with $S_i \subset S_j$ is viewed as directed edge from $S_i$ to $S_j$. In this view, all the edges of Figure 3.1 are directed towards the top. The following observation is now optional.

![Figure 3.1: Boolean Lattice $G_4$](image)
Lemma 3.1. If \( S_0 \subset S_1 \subset \cdots \subset S_{u-1} \) then the totally-ordered set \( S = \{ S_0, S_1, \cdots, S_{u-1} \} \) corresponds to a path in \( G \) traversing \( S_0, S_1, \cdots, S_{u-1} \) in that order.

In Definition 2.4 (see page 2.4) we described how a source set \( L \) and a set of ordered partitions \( \Pi \) produce an output set \( S = L \circ \pi \). Now in Section 3.2 we further study the relationship between the source set \( L \) and the output set \( S \), given that one of them is a totally-ordered set. In Section 3.3 we first define a canonical form for the source words in a totally-ordered source set. Then we show that every totally-ordered source set can be assumed to be in canonical form. This representation makes many of the proofs starting from Chapter 4 more concise.

3.2 Totally-Ordered Source and Output Sets

Let \( \mathcal{L} = \{ L_0, L_1, \cdots, L_{X-1} \} \) be a set of \( z \) bit source words and let \( \pi = \langle B_0, B_1, \cdots, B_{z-1} \rangle \) be an ordered \( z \) ordered partition of \( \mathbb{Z}_n \). Let \( S = \mathcal{L} \circ \pi = \{ S_0, S_1, \cdots, S_{u-1} \} \) be a set of \( u \) subsets of \( \mathbb{Z}_n \), where \( u \leq X \).

Lemma 3.2. If \( \mathcal{L} \) is a totally-ordered set then \( S = \mathcal{L} \circ \pi \) is a totally-ordered set.

Proof. We proceed in the contrapositive direction. Suppose \( S \) is not a totally-ordered set. This implies that there exist different subsets \( S_i, S_j \in S \) such that \( S_i \not\subset S_j \) and \( S_j \not\subset S_i \). This further implies that there exist elements \( a, b \in \mathbb{Z}_n \) such that \( a \in S_i, a \not\in S_j \) and \( b \in S_j, b \not\in S_i \). For blocks \( B_q, B_r \) of ordered partition \( \pi \), let \( a \in B_q \not\in \pi \) and \( b \in B_r \not\in \pi \); observe that \( q \neq r \), otherwise \( a, b \in B_q \in \pi \) and \( a \in S_i \) if and only if \( b \in S_i \). For some source words \( L_i, L_j \in \mathcal{L} \), let \( S_i = L_i \circ \pi \), and \( S_j = L_j \circ \pi \). Now \( a \in S_i \), and \( b \not\in S_i \) implies that \( L_i(q) = 1 \) and \( L_i(r) = 0 \) (see definition 2.4 on page 10) or \( q \in L_i \) and \( r \not\in L_i \). Similarly \( b \in S_j \), and \( a \not\in S_j \) implies that \( L_j(r) = 1 \) and \( L_j(q) = 0 \) (chapter 2) or \( r \in L_j \) and \( q \not\in L_j \). Thus \( L_i \not\subset L_j \) and \( L_j \not\subset L_i \), implying that \( \mathcal{L} \) is also not a totally-ordered set.

Example 3.2. Let \( S_i = \{ S_{i_0}^i, S_{i_1}^i, \cdots, S_{X-1}^i \} \) not be a totally-ordered set, and \( S_{i_0}^i = \)}
We can say from the above values that $A \in S_0^i, A \notin S_1^i$, similarly $B \notin S_0^i, B \in S_1^i$. Now assuming the output set is a result of the following computation $L \circ \pi_i = S_i$ (definition 2.4), let the ordered partition $\pi_i = \{\{B\}, \{A\}, \{C, E, G, I, K, M, O\}, \{D, F, H, J, L, N\}\}$, and hence the corresponding $L = \{L_0, L_1, \cdots , L_{X-1}\}, L_0 = 0110$ and $L_1 = 1010$ where $L_0 \circ \pi_i = S_0^i$ and $L_1 \circ \pi_i = S_1^i$. Hence $L_0 \not\subseteq L_1$ and $L_1 \not\subseteq L_0$ implying $L$ is not a totally-ordered set.

We now proceed to a similar result in the opposite direction.

**Lemma 3.3.** Let $L \circ \pi = S$, let $\pi$ be an ordered partition with no empty blocks. If $S$ is a totally-ordered set then $L$ is a totally-ordered set.

**Proof.** Again we proceed in the contrapositive direction. Suppose that $L$ is not totally-ordered. This implies that there exist distinct source words $L_i, L_j \in L$ such that $L_i \not\subseteq L_j$ and $L_j \not\subseteq L_i$. This further implies that there exist elements $q, r \in \mathbb{Z}_z$ such that $q \in L_i, q \not\in L_j$ and $r \in L_j, r \not\in L_i$. We can further say that $L_i(q) = 1, L_i(r) = 0$ and $L_j(r) = 1, L_j(q) = 0$. Let $a \in B_q \subseteq \pi$ and $b \in B_r \subseteq \pi$. (The existence of $a, b$ is assured as $B_q, B_r \neq \emptyset$.) This further implies that $a \in S_i = L_i \circ \pi, a \notin S_j = L_j \circ \pi$ and $b \in S_j = L_j \circ \pi, b \notin S_i = L_i \circ \pi$. Hence we can conclude that $S$ is not a totally-ordered set.

**Example 3.3.** Let $L = \{L_0, L_1, \cdots , L_{X-1}\}, L_0 = 0110$ and $L_1 = 1010$ where $L_0 \circ \pi_i = S_0^i$ and $L_1 \circ \pi_i = S_1^i$. Now $L_0(2) = 1, L_0(3) = 0$ and $L_1(3) = 1, L_1(2) = 0$. Let $\pi_i =$
\{\{B\}, \{A\}, \{C, E, G, I, K, M, O\}, \{D, F, H, J, L, N\}\}. Assuming \(S_i = \{S_{i0}, S_{i1}, \ldots, S_{iX-1}\}\) the corresponding \(S_{i0} = \)

\[
\begin{array}{cccccccccccc}
1 & 0 & 1 & 0 & 1 & 0 & 1 & 0 & 1 & 0 & 1 & 0 \\
A & B & C & D & E & F & G & H & I & J & K & L \\
\end{array}
\]

\(S_{i1} = \)

\[
\begin{array}{cccccccccccc}
0 & 1 & 1 & 0 & 1 & 0 & 1 & 0 & 1 & 0 & 1 & 0 \\
A & B & C & D & E & F & G & H & I & J & K & L \\
\end{array}
\]

Hence \(S_{i0} \not\subseteq S_{i1}\) and \(S_{i1} \not\subseteq S_{i0}\) implying \(S_i\) is not a totally-ordered Set.

### 3.3 Canonical Form of Source Set

We first define a reverse sorted string.

**Definition 3.2.** A binary string \(\langle a_0, a_1, \ldots, a_{m-1}\rangle\) is reverse sorted if and only if every 1 in the string precedes any 0.

**Example 3.4.** For \(m = 8\), \(\overrightarrow{L_1} = \langle 11100000 \rangle\) is reverse sorted whereas \(\overrightarrow{L_2} = \langle 10110000 \rangle\) and \(\overrightarrow{L_3} = \langle 0000011 \rangle\) are not.

Recall from Definition 2.1 (see page 8) that the characteristic string of a set \(L \subseteq \mathbb{Z}_z\) depends on the characteristic order \(\overrightarrow{c}\) assumed on \(\mathbb{Z}_z\).

**Definition 3.3.** Let \(\mathcal{L} \subseteq \mathcal{P}(\mathbb{Z}_z)\), and let \(\overrightarrow{c} = \langle c_0, c_1, \ldots, c_{z-1}\rangle\) be a characteristic order of \(\mathbb{Z}_z\). The characteristic order \(\overrightarrow{c}\) is in canonical form if and only if the characteristic string \(\overrightarrow{L}\) is reverse sorted for every \(L \in \mathcal{L}\).

**Example 3.5.** For \(z = 8\) and \(\mathcal{L} = \{L_0, L_1\}\), let \(L_0 = \{1, 3, 5\}\) and \(L_1 = \{1, 2, 3, 4, 5\}\). Consider the characteristic order \(\overrightarrow{c_1} = \langle 0, 1, 2, 3, 4, 5, 6, 7 \rangle\), \(\overrightarrow{c_2} = \langle 6, 1, 2, 3, 4, 5, 0, 7 \rangle\), \(\overrightarrow{c_3} = \langle 6, 1, 2, 3, 4, 5, 0, 7 \rangle\).
\langle 1, 3, 5, 2, 4, 0, 6, 7 \rangle \text{ and } c^*_4 = \langle 5, 1, 3, 4, 2, 6, 0, 7 \rangle. \text{ Under } c^*_1 \text{ we have } L_0 = \langle 01010100 \rangle \text{ and } L_1 = \langle 01111100 \rangle. \text{ Now characteristic order } c^*_1 \text{ gives the exact same characteristic representation of } L_0, L_1. \text{ For characteristic order } c^*_2 \text{ we have rearranged the positions of elements of } \mathbb{Z}_n. \text{ However the characteristic string of } L_0 \text{ and } L_1 \text{ are still the same as under } c^*_1. \text{ Now for } c^*_3 \text{ we see that the positions are rearranged in such a way that the resulting characteristic string produces } L_0 = \langle 11100000 \rangle \text{ and } L_1 = \langle 11111000 \rangle. \text{ Here all } 1's \text{ precede all the } 0's, \text{ which is the reverse sorted order. For } c^*_4, \text{ we see that the characteristic strings of } L_0 \text{ and } L_1 \text{ are the same as under } c^*_3 \text{ and hence reverse sorted order. Thus } c^*_3 \text{ and } c^*_4 \text{ are in canonical form whereas } c^*_1 \text{ and } c^*_2 \text{ are not. We note } c^*_3 \neq c^*_4. \hfill \square

We now show that there is no loss of generality in assuming a canonical order for the source set \( \mathcal{L} \).

Let \( \mathcal{L} = \{L_0, L_1, \ldots, L_{X-1}\} \) be totally-ordered with \( L_0 \subset L_1 \subset \cdots \subset L_{X-1} \). We now construct a characteristic order \( \overrightarrow{c} = \langle c_0, c_1, \ldots, c_{z-1} \rangle \) as described below. Let \( |L_0| = m_0, \ |L_i - L_{i-1}| = m_i \) for \( 0 < i < X \) and let \( n_i = m_0 + m_1 + \cdots + m_i = |L_i| \). In constructing \( \overrightarrow{c} \) we enumerate the \( m_0 \) elements of \( L_0 \) first; that is \( L_0 = \{c_0, c_1, \ldots, c_{m_0-1}\} \). The relative order of these \( m_0 \) elements is irrelevant. Next for \( 0 < i < X \), we assign the \( m_i \) elements of \( L_i - L_{i-1} \) to elements of \( \{c_{n_{i-1}}, c_{n_{i-1}+1}, \ldots, c_{n_i-1}\} \). We will call a characteristic order \( \overrightarrow{c} \) a standard characteristic order.

**Theorem 3.1.** Every standard characteristic order is canonical for totally-ordered \( \mathcal{L} \)

**Proof.** Without loss of generality let \( \mathcal{L} = \{L_0, L_1, \ldots, L_{X-1}\} \) with \( L_0 \subset L_1 \subset \cdots \subset L_{X-1} \) and \( |L_i| = n_i \), for \( 0 \leq i < X \). Let \( \overrightarrow{c} = \langle c_0, c_1, \ldots, c_{z-1} \rangle \) be a standard characteristic order.

From our construction we can say as follows:

\[
\overrightarrow{c} = \left\langle \begin{array}{c}
\underbrace{c_0, c_1, \ldots, c_{n_{i-1}}, c_{n_0}, c_{n_0+1}, \ldots, c_{n_1-1}, \ldots, c_{n_{i-1}+1}, \ldots, c_{n_i-1}, \ldots}
\end{array}
\right\rangle_{L_0} \right\rangle_{L_1-L_0} \right\rangle_{L_i-L_{i-1}} \left\langle \begin{array}{c}
\underbrace{c_{n_{X-2}}, c_{n_{X-2}+1}, \ldots, c_{n_{X-1}-1}}
\end{array}
\right\rangle_{L_{X-1}-L_{X-2}} (3.1)
\]
Hence the representation of $L_i$ under $\vec{c}$ has the following form:

$$\vec{c} = \left\langle \begin{array}{c} 1,1,\ldots,1,1,\ldots,1,0,0,\ldots,0,0,\ldots,0 \\ L_0 \quad L_1-L_0 \quad L_i-L_{i-1} \quad L_{i+1}-L_i \quad L_{X-1}-L_{X-2} \end{array} \right\rangle$$

which is reverse sorted (definition 3.2).

Thus we may, without any loss of generality assume that every totally ordered source set $\mathcal{L}$ is represented in canonical order. This assumption does not change $\mathcal{L}$, it only makes our proof in subsequent chapters much easier.
Chapter 4
Generating a Given Totally Ordered Set

Let $S = \mathcal{L} \circ \pi$ be any set of output subsets that can be produced from a source set $\mathcal{L}$ and a single ordered partition $\pi$. In this chapter, we will first produce multiple ordered partition $\pi_0, \pi_1, \cdots, \pi_y$ for totally-ordered $S$. This result is important as the entire result of Jordan and Vaidyanathan [16] applies only to a relatively small set $S$ with at most $X = z - 1$ elements. Further, we show that any decomposition of $S$ into $S_0, S_1, \cdots, S_{k-1}$ works, as long as $|S_i| \leq X$.

In a separate result, Jordan and Vaidyanathan [16] showed that a set $S = S_0 \cup S_1 \cup \cdots \cup S_{k-1}$ of isomorphic totally-ordered sets $S_0, S_1, \cdots, S_{k-1}$ with $|S_i| \leq X$ can be implemented as $\text{MU}(x, x + 1, \log_k n)$. We extend this to work for any set $S = S_0 \cup S_1 \cup \cdots \cup S_{k-1}$ of totally-ordered sets (not necessarily isomorphic).

4.1 Generating a Large Totally-Ordered Set

We begin with the case where a totally-ordered set $S$ is small. Recall that $\mathcal{L}$ is the source set. Suppose that $|\mathcal{L}| \geq |S|$. In Lemma 3.3 we showed that if $S = \mathcal{L} \circ \pi$ then if $S$ is totally-ordered, then so is $\mathcal{L}$ (assuming $\pi$ has no empty block). The following lemma, in a way, works in the opposite direction.

Lemma 4.1. For any given $\mathcal{L}$ and $S$ that are both totally-ordered sets, with $|\mathcal{L}| \geq |S|$ there exists an ordered partition $\pi$ such that $\mathcal{L} \circ \pi = S$. \hfill $\Box$

Proof. Let $\mathcal{L} = \{L_0, L_1, \cdots, L_{X-1}\}$ with $L_i \subset L_{i+1}$ for $0 \leq i < X-1$ and $S = \{S_0, S_1, \cdots, S_{u-1}\}$ with $S_i \subset S_{i+1}$ for $0 \leq i < u-1$. Here $u \leq X$. Recall that $z$ is the source word length for $L_i$ to be distinct. $X \leq z - 1$, assuming $L_i \neq \emptyset$ or $\mathbb{Z}_z$. Without loss of generality assume that $\mathcal{L}$ is in canonical form. Then it is easy to see that $L_i = \left[ \begin{array}{c} 111 \ldots 111 \ 000 \ldots 000 \end{array} \right]_{i+1 \ z-i}$. Construct ordered partition $\pi = \{B_i : 0 \leq i < z\}$ as follows: block $B_0 = S_0$ and for $0 < i < u$, $B_i = S_i$.
$B_i = S_i - S_{i-1}$. Elements of $\mathbb{Z}_n - S_{u-1}$ can be placed in any manner among blocks $B_u$ to $B_{z-1}$. Observe that $\bigcup_{j=0}^{i} B_j = S_i$. Consider any source word $L_i = \langle 111\ldots111_{i+1}000\ldots000_{z-(i+1)} \rangle$ where $0 \leq i < u$. $a \in L_i \circ \pi$ if and only if $a \in B_0 \cup B_1 \cup \cdots \cup B_i = S_i$. That is $S_i = L_i \circ \pi$. Therefore $S = L \circ \pi$.

The above result also appears in Jordan and Vaidyanathan [16], although expressed less formally. This result extends in a simple manner to large totally-ordered sets $S$ with greater than $|L|$ elements. Ordered partition $S$ into non-empty sets $S_0, S_1, \ldots, S_{v-1}$ such that $\bigcup_{j=0}^{v-1} S_j = S$ and $S_i \cap S_j = \emptyset$ for $i \neq j$. Any such ordered partition suffices since $S$ is totally-ordered, clearly each $S_i$ is totally-ordered.

Let $|S_i| \leq |L|$ so $v \geq \lceil \frac{|S|}{|L|} \rceil$. For each $S_i$, applying Lemma 2.4 gives us an ordered partition $\pi_i$ such that $S_i = L \circ \pi_i$. Thus with $\Pi = \{\pi_i : 0 \leq i < v\}$, we get $S = L \circ \Pi$.  

**Lemma 4.2.** For any totally-ordered output set $S$ and a totally-ordered source set $L$ there exists a set of $v$ ordered partitions $\pi$ such that $S = L \circ \Pi$ where $v \geq \lceil \frac{|S|}{|L|} \rceil$.

**Theorem 4.1.** A MU$(x, 2^x + 1, y, n)$ can produce a totally-ordered set of at most $2^x + y$ subsets.

**Proof.** Setting $|L| = 2^x$ and $v = 2^y$ in Lemma 4.2, we have $|S| \leq v|L| = 2^x + y$.

This result extends the idea in Jordan and Vaidyanathan [16] to large totally-ordered sets. The above theorem shows a method to implement a large totally-ordered set $S$ or a MU-Decoder. In doing so, we partitioned $S$ into $2^x$ blocks. Clearly, it is useful to have each block of $S$ contain approximately the same number of elements, so that the number of blocks is reduced. Thus $S = \bigcup_{i=0}^{v-1} S_i$ where $v = \lceil \frac{|S|}{|L|} \rceil$ and $|S_i| \leq \lceil \frac{|S|}{v} \rceil \leq |L|$.

However, which elements of $S$ are in $S_i$ is not clear. For the purpose of the results in this chapter, this question is irrelevant. However, in Chapter 5 we will show that a particular way of constructing $S_i$ is advantageous.
4.2 Generating a Set of Non-isomorphic Totally Ordered Sets

Let $S = \bigcup_{i=0}^{v-1} S_i$ be a set of output sets with $S_i \neq \emptyset$, $S_i \cap S_j = \emptyset$ for $i \neq j$ and $S_i$ is totally-ordered. Then recall Definition 2.6 that $S$ is a set of isomorphic totally-ordered sets if and only if for each $S_i$, $S_j$ and any $S^i \in S_i$ there exists an $S^j \in S_j$ such that $|S^i| = |S^j|$. Jordan and Vaidyanathan [16] proved that if $S$ is a set of isomorphic totally-ordered sets and if $|S_i| \leq |\mathcal{L}|$ then an MU-Decoder can generate $S$ as in Definition 2.6.

The main contribution of this definition is to leverage a common $\mathcal{L}$ for all $v = 2^y$ totally-ordered sets. We now show that the isomorphic restriction is not needed and that the range of ordered partition could exceed $v$.

**Theorem 4.2.** Let $S = \bigcup_{i=0}^{v-1} S_i$ be a set of output sets where $S_i$ is non-empty totally-ordered and pairwise disjoint. Then for any totally-ordered source set $\mathcal{L}$, there exists a set $\Pi$ of at most $v + \left\lceil \frac{|S|}{|\mathcal{L}|} \right\rceil$ ordered partitions such that $S = \mathcal{L} \circ \Pi$.

**Proof.** By Lemma 2.4 each $S_i$ can be generated by $\mathcal{L}$ and an ordered partition set $\pi_i$ (so $S_i = \mathcal{L} \circ \pi_i$). Here $|\pi_i| = \left\lceil \frac{|S_i|}{|\mathcal{L}|} \right\rceil$. Therefore the total number of ordered partition s needed for $S = \bigcup_{i=0}^{v-1} S_i$ is at most

$$\sum_{i=0}^{v-1} |\pi_i| = \sum_{i=0}^{v-1} \left\lceil \frac{|S_i|}{|\mathcal{L}|} \right\rceil < \sum_{i=0}^{v-1} 1 + \frac{|S_i|}{|\mathcal{L}|} = v + \frac{1}{|\mathcal{L}|} \sum_{i=0}^{v-1} |S_i| = v + \frac{|S|}{|\mathcal{L}|}.$$  

The above is used later for Generic Subset Generation.
Chapter 5
Hardware Enhancement for Partition Generation

In Chapter 2 we discussed the structure of the MU-Decoder (Figure 2.1, Page 9). In this chapter, we particularly focus on the selector module of the MU-Decoder. The selector module accepts a selector address and selects the corresponding ordered partition to be used with the MUXes for mapping the source words. Originally each selector address produces one ordered partition. In this chapter, we propose hardware enhancement for the selector module that allows each selector address to produce multiple ordered partitions.

The original selector module produces $2^y$ ordered $z$-partitions with a hardware cost of $O(2^y n \log z)$, where $n$ is the size of the output word representing an output subset of $\mathbb{Z}_n = \{0, 1, \ldots, n - 1\}$. With the enhancements, we will be able to produce $k2^y$ ordered $z$-partitions with a cost of $O(2^y n \log h)$; that is with the same cost as before, we can produce a factor of $k$ more ordered partitions.

In the next section we provide an overview of the new hardware. Section 5.1 is devoted to the idea of “Translation” of one ordered partition to another. This operation is essential to our hardware enhancement.

![Figure 5.1: Current Selector Module](image)

Figure 5.1: Current Selector Module
5.1 Ordered Partition Translation

In this section we describe an operation called translation on an ordered partition π₀ that produces another ordered partition π₁. Central to this operation is a “Translation Vector” \( \vec{C} \) that guides the translation of π₀ to π₁. Subsequently we will develop the proposition of translation.

**Definition 5.1.** Let ordered partition \( \pi_0 = \{B^0_m : 0 \leq m < z\} \) be an ordered partition on \( \mathbb{Z}_n \), with \( |B^0_m| = \ell^0_m \). For each \( 0 \leq m < z \), let \( C_m \subseteq B^0_m \) with \( C_{z-1} = \emptyset \). Denote \( |C_m| = c_m \).

Let vector \( \vec{C} = \{C_m : 0 \leq m < z\} \). Ordered partition \( \pi_1 = \{B^1_m : 0 \leq m < z\} \) is a unit-translation of \( \pi_0 \) with respect to vector \( \vec{C} \) (or \( \pi_0 \xrightarrow{\vec{C}} \pi_1 \)) if and only if \( B^1_0 = B^0_0 - C_0 \) and for all \( 0 < m < z \), \( B^1_m = (B^0_m - C_m) \cup C_{m-1} \).

**Example 5.1.** Let ordered partition \( \pi_0 = \{\{3, 4, 11, 12\}, \{2, 5, 10, 13\}, \{1, 6, 9, 14\}, \{0, 7, 8, 15\}\} \) and ordered partition \( \pi_1 = \{\{3, 4\}, \{11, 12, 2, 5, 10\}, \{13, 1\}, \{6, 9, 14, 0, 7, 8, 15\}\} \). Then \( \pi_0 \xrightarrow{\vec{C}} \pi_1 \), that is \( \pi_1 \) is a unit-translation of \( \pi_0 \) with respect to vector \( \vec{C} = \{\{11, 12\}, \{13\}, \{6, 9, 14\}, \emptyset\} \).

Let ordered partition \( \pi_2 = \{\{3, 4, 11, 13\}, \{2, 5, 10, 12\}, \{1, 6, 9, 15\}, \{0, 7, 8, 14\}\} \) then \( \pi_0 \not\xrightarrow{\vec{B}} \pi_2 \), because \( B^2_0 \neq B^0_0 - D_0 \) for any \( D_0 \subseteq B^0_0 \) we have \( B^2_0 \not\subseteq B^0_0 \). Moreover though \( B^2_1 = (B^0_1 - D_1) \cup D_0 \) where \( D_0 = \langle 12 \rangle, D_1 = \langle 13 \rangle \). But \( B^2_2 \neq (B^0_2 - D_2) \cup D_1, B^2_3 \neq (B^0_3 - D_3) \cup D_1 \) and \( D_3 \neq \emptyset \).

Let ordered partition \( \pi_3 = \{\{3, 4\}, \{11, 12, 2\}, \{5, 10\}, \{13, 1, 6, 9, 14, 0, 7, 8, 15\}\} \), then \( \pi_1 \xrightarrow{\vec{E}} \pi_3 \), that is \( \pi_3 \) is a unit-translation of \( \pi_1 \) with respect to vector \( \vec{E} = \{\emptyset, \{5, 10\}, \{13, 1\}, \emptyset\} \).

We also note that \( \pi_0 \not\xrightarrow{\vec{F}} \pi_3 \), because \( \langle 13 \rangle \in B^0_0 \), so it should be in \( B^3_1 = (B^0_1 - F_1) \cup F_0 \) or \( B^3_2 = (B^0_2 - F_2) \cup F_1 \) but \( \langle 13 \rangle \in B^3_3 \) which is not possible through unit-translation.

We observe that when \( \pi_0 \) is translated to \( \pi_1 \), \( c_m \leq |B^0_m| \) elements of \( B^0_m \in \pi_0 \) move to \( B^1_{m+1} \in \pi_1 \). All the remaining elements remain in the same block, that is the rest of the elements of \( B^0_m \in \pi_0 \) move to \( B^1_m \in \pi_1 \) which is basically the same block number across different partitions.

**Definition 5.2.** For each element \( a \in \mathbb{Z}_n \), let \( r(a) \) be a unique rank (number) from \( \mathbb{Z}_n \).
The value $a$ and its rank $r(a)$ are not related. A set of ranks $r$ is consistent with an ordered partition $\pi = \langle B_0, B_1, \ldots, B_{z-2}, B_{z-1} \rangle$ if and only if the following condition holds. For all $a \in B_i$ and $b \in B_j$, if $i < j$ then $r(a) < r(b)$ and if $a, b \in B_i^0$ still $r(a) \neq r(b)$.

In Example 5.1, $\pi_0 \xrightarrow{c_1} \pi_1 \xrightarrow{c_2} \pi_3$ is a 2-transition with ordered partition $\pi_0 = \langle \{3, 4, 11, 12\}, \{2, 5, 10, 13\}, \{1, 6, 9, 14\}, \{0, 7, 8, 15\} \rangle$ and the rank of the ordered partition $r(\pi_0) = \langle \{0, 1, 2, 3\}, \{4, 5, 6, 7\}, \{8, 9, 10, 11\}, \{12, 13, 14, 15\} \rangle$.

**Definition 5.3.** Let $\pi_0 \xrightarrow{c_0} \pi_1 \xrightarrow{c_1} \pi_2 \cdots \pi_{k-1} \xrightarrow{c_{k-1}} \pi_k$ be a series of unit-translations. This sequence of $k$ unit-translations from $\pi_0$ to $\pi_k$ will be called a $k$-translation if and only if there exists a rank $r$ for each element of $\mathbb{Z}_n$ that is consistent with every ordered partition $\pi_i$ ($0 \leq i \leq k$).

This implies that the $C_m$ elements of $B_m^0$ that move into $B_{m+1}^1$ are the highest ranked elements of $B_m^0$.

**Lemma 5.1.** Suppose $\pi_0 \xrightarrow{c} \pi_1$. Then for all $a \in \mathbb{Z}_n$, if $a \in B_m^0$ then $a \in B_{m'}^1$, where $m' \in \{m, m+1\}$.

**Proof.** Let position $a \in B_m^0$. Now every block in ordered partition $\pi_1$ is $B_m^1 = (B_m^0 - C_m) \cup C_{m-1}$, where $C_{-1} = \emptyset$. Since $a \in B_m^0$, $a \not\in B_{m-1}^0$ if it exists (the blocks are disjoint). That is $a \not\in C_m \in B_{m-1}^0$. Now we consider two cases, first if $a \in C_m$, then $a \not\in B_m^1$ but $a \in B_{m+1}^1 = (B_{m+1}^0 - C_{m+1}) \cup C_m$. Second if $a \not\in C_m$ then $a \in B_m^1 = (B_m^0 - C_m) \cup C_{m-1}$.

Remark: The block number of any element of block $B_m^0$ increases by at most 1 as we translate from $\pi_0$ to $\pi_1$.

**Theorem 5.1.** Let $\mathcal{L}$ be a source set and $\pi_0, \pi_1$ be ordered $z$-partitions such that $\pi_0 \xrightarrow{c} \pi_1$. Let output sets $S_0 = \mathcal{L} \circ \pi_0$ and $S_1 = \mathcal{L} \circ \pi_1$. If $\mathcal{L}$ is totally-ordered, then $S_0 \cup S_1$ is totally-ordered.

25
Proof. Without loss of generality, we assume that the source set \( L \) is in canonical form (see Section 3.3 on page 17). Clearly, by Lemma 3.2 (Page 15), \( S_0 \) and \( S_1 \) are independently totally-ordered, as \( L \) is totally-ordered. Suppose that \( S_0 \cup S_1 \) is not totally-ordered. Then there exist subsets \( S_0^0 \subseteq S_0 \) and \( S_1^1 \subseteq S_1 \) such that \( S_0^0 \nsubseteq S_1^1 \) and \( S_1^1 \nsubseteq S_0^0 \). This implies that there are elements \( a, b \in \mathbb{Z}_n \) such that \( a \in S_0^0 \), \( b \notin S_0^0 \) and \( a \notin S_1^1 \), \( b \in S_1^1 \). This implies that \( a, b \) are in different blocks of \( \pi_0 \); similarly they are in different blocks of \( \pi_1 \). Let \( a \in B_0^0 \), \( b \in B_1^1 \) where \( \hat{i} \neq \tilde{i} \) and let \( a \in B_1^1 \) and \( b \in B_1^1 \) where \( \hat{j} \neq \tilde{j} \). Let \( S_i^0 = L_{i'} \circ \pi_0 \) and \( S_j^0 = L_{j'} \circ \pi_1 \). Since \( a \in S_i^0 \), \( a \in B_1^0 \) and \( S_i^0 = L_{i'} \circ \pi_0 \), we have \( L_{i'}(\hat{i}) = 1 \). Similarly \( L_{i'}(\hat{j}) = 0, L_{j'}(\tilde{j}) = 0 \) and \( L_{j'}(\tilde{j}) = 1 \). Now since source set \( L \) is in canonical form, from \( L_{i'}(\hat{i}) = 1 \) and \( L_{j'}(\tilde{j}) = 0 \) we can say

\[
\hat{i} < \tilde{i} \quad (5.1)
\]

similarly \( \hat{j} > \tilde{j} \quad (5.2) \)

Now since \( a \in B_0^0 \) and \( a \in B_1^1 \), \( a \) might have moved to a new block in translation.

by Lemma 5.1 \( \hat{i} \leq \hat{j} \leq \hat{i} + 1 \quad (5.3) \)

Similarly \( \tilde{i} \leq \tilde{j} \leq \tilde{i} + 1 \quad (5.4) \)

Hence we can say \( \tilde{i} \leq \tilde{j} \leq \hat{i} \leq \hat{j} \leq \hat{i} + 1 \leq \tilde{i} \) which is a contradiction. Hence the lemma.

\[\square\]

**Lemma 5.2.** Let \( G_{j+1}^1 = B_0^1 \cup B_1^1 \cup \cdots \cup B_{j+1}^1 \) and \( G_{j+1}^0 = B_0^0 \cup B_1^0 \cup \cdots \cup B_{j+1}^0 \). Then \( G_{j+1}^1 = G_{j+1}^0 - C_{j+1} \), where \( 0 \leq j + 1 < z - 1 \).

\[\square\]

**Proof.** As assumed \( G_{j+1}^1 = B_0^1 \cup B_1^1 \cup \cdots \cup B_{j+1}^1 \) and \( G_{j+1}^0 = B_0^0 \cup B_1^0 \cup \cdots \cup B_{j+1}^0 \). From Definition 5.1 we know that \( B_0^1 = B_0^0 - C_0 \) and for all \( 0 < j < z \), \( B_j^1 = (B_j^0 - C_j) \cup C_{j-1} \).
where \( C_{z-1} = \emptyset \).

Hence
\[
G_{j+1}^1 = B_0^1 \cup B_1^1 \cup \cdots \cup B_{j+1}^1
\]
\[
= \{(B_0^0 - C_0)\} \cup \{(B_1^0 - C_1) \cup C_0\} \cup \cdots \cup \{(B_{j+1}^0 - C_{j+1}) \cup C_{j}\}
\]
\[
= B_0^0 \cup B_1^0 \cup \cdots \cup (B_{j+1}^0 - C_{j+1})
\]
\[
= C_j \cup (B_{j+1}^0 - C_{j+1})
\]

Since all blocks are disjoint and \( C_{j+1} \subseteq B_{j+1}^0 \)

Hence:
\[
G_{j+1}^1 = G_{j+1}^0 - C_{j+1}
\]

moreover
\[
G_{j+1}^1 \subset G_{j+1}^0
\]

Now since \( C_{z-1} = \emptyset \) and union of all the blocks gives us \( Z_n \)

we have:
\[
G_{z-1}^1 = G_{z-1}^0 = Z_n
\]

Hence the equation
\[
G_{j+1}^1 = G_{j+1}^0 - C_{j+1}
\]
(5.5)
proves the lemma.

Let us again consider the translation \( \pi_0 \xrightarrow{c_i} \pi_1 \), which yields the output set \( S_0 = L \circ \pi_0 \) and \( S_1 = L \circ \pi_1 \). From Theorem 5.1 we know that if source set \( L \) is totally-ordered then \( S_0 \cup S_1 \) is totally-ordered as well. We now derive the circumstances under which \( S_0 \) and \( S_1 \) are disjoint.

**Lemma 5.3.** Let \( \pi_0 \xrightarrow{c_i} \pi_1 \) and for totally-ordered source set \( L \), let \( S_0 = L \circ \pi_0 \) and \( S_1 = L \circ \pi_1 \). Then if \( 0 < c_i < |B_i^0| \) for all \( 0 \leq i < z \) we have \( S_0 \cap S_1 = \emptyset \), where \( C_{z-1} = \emptyset \). □

**Proof.** We first observe that since \( 0 < c_i < |B_i^0| \) for \( 0 \leq i < z - 1 \), we have non empty blocks for \( \pi_0 \). We proceed in the contrapositive direction. Let \( S \in S_0 \cap S_1 \). Then there exists some \( 0 \leq u', v' < X \) such that \( S = L_{u'} \circ \pi_0 = L_{v'} \circ \pi_1 \). We can, without loss of generality, assume the canonical form for source set \( L \) and say \( L_{u'} = \begin{pmatrix} 111... & 111000...000 \\ u & z-u \end{pmatrix} \) and \( L_{v'} = \begin{pmatrix} 111... & 111000...000 \\ v & z-v \end{pmatrix} \). Using Definition 2.4 we can say \( L_{u'} \circ \pi_0 = \bigcup_{i=0}^{z-1} [1(B_i^0, L_{u'}(i))] = \)
\{B^0_u \cup B^0_1 \cup \ldots \cup B^0_v\} = G^0_u \text{ (Lemma 5.2)}. \text{ Similarly}

\[L_v \circ \pi_1 = \bigcup_{i=0}^{z-1} [1(B^1_i, L_v'(i))] = \{B^1_0 \cup B^1_1 \cup \ldots \cup B^1_v\} = G^1_v. \text{ Hence } S = G^0_u = G^1_v.

Hence

\[S = G^0_u = G^1_v \quad (5.6)\]

\[G^1_v = G^0_v - C_v\]

\[G^0_u = G^0_v - C_v \quad (5.7)\]

We now consider a few cases:

i If \(u = v\) then we can state equations (5.5),(5.6) as \(G^0_v (5.6) = G^1_v (5.5) = G^0_v - C_v. \text{ Hence } C_v = \emptyset.\)

This is not possible as \(c_v > 0.\)

ii For \(u > v\) let the union of \(u\) blocks \(G^0_u = \{B^0_0 \cup B^0_1 \cup \ldots \cup B^0_v \cup B^0_{v+1} \cup \ldots \cup B^0_u\} = G^0_v \cup \{B^0_{v+1} \cup B^0_{v+2} \cup \ldots \cup B^0_u\}.\text{ Now } B^0_v \neq \emptyset. \text{ Hence } G^0_u \supset G^0_v \supset G^1_v \supset G^0_u, \text{ which is the necessary contradiction.} \]
iii For $u < v$, let $u + i = v$ where $i > 0$ is an integer.

$$G_u^0 = C_v^0 - C_v$$

**Equation (5.7)**

$$= G_{u+i}^0 - C_{u+i}$$

$$= \left[ G_u^0 \cup \left( B_{u+1}^0 \cup B_{u+2}^0 \cup \cdots \cup B_{u+i}^0 \right) \right]_{H \ (\text{let})} - C_{u+i}$$

$$= (G_u^0 - C_{u+i}) \cup (H - C_{u+i})$$

Now since $C_{u+i} \subseteq B_{u+i}^0 \notin G_u^0$ we can say

$$G_u^0 = G_u^0 \cup (H - C_{u+i})$$

Hence $H - C_{u+i} \subseteq C_u^0$

This is possible only if $H - C_{u+i} = \emptyset$ because all the blocks of an ordered partition are disjoint, meaning $H \cap G_u^0 = C_{u+i} \cap G_u^0 = \emptyset$

From $H - C_{u+i} = \emptyset$

we can say $H \subseteq C_{u+i}$

Now since all the blocks are non-empty and $H = \left( B_{u+1}^0 \cup B_{u+2}^0 \cup \cdots \cup B_{u+i}^0 \right)$ we can say

$$B_{u+i}^0 \subset H \subseteq C_{u+i}$$

Therefore $|B_{u+i}^0| < c_{u+i}$,

which contradicts our assumption that $c_{u+i} < |B_{u+i}^0|$.

\[\square\]

**Lemma 5.4.** Let $\pi_0 \xrightarrow{\mathcal{C}} \pi_1$ and for totally-ordered source set $\mathcal{L}$, let $S_0 = \mathcal{L} \circ \pi_0$ and $S_1 = \mathcal{L} \circ \pi_1$. Then $S_0 \cap S_1 = \emptyset$ implies for all $0 < m < z - 1$, $0 < c_m < |B_m^0|$.

\[\square\]

**Proof.** Suppose the conclusion is false. That is for all $0 < m < z - 1$, $0 < c_m < |B_m^0|$ is false. This implies that there exists $m$ such that $c_m = |B_m^0|$ or $c_m = 0 \neq |B_m^0|$. 

29
Lemma 5.5. Let \( \pi_0 \xrightarrow{C} \pi_1 \) and for totally-ordered source set \( \mathcal{L} \), let \( S_0 = \mathcal{L} \circ \pi_0 \) and \( S_1 = \mathcal{L} \circ \pi_1 \). Then for all \( 0 < m < z-1 \) we have \( 0 < c_m < |B^0_m| \) if and only if \( S_0 \cap S_1 = \emptyset \).

Proof. Evident from the results of the two Lemmas 5.3 and 5.4.

We now consider a particular class of translation \( \pi_0 \xrightarrow{C} \pi_1 \), in which for all \( 0 \leq m < z-1, \) \( |B^0_v| \neq \emptyset \) implies \( 0 < c_m = c \leq |B^0_m| \). That is \( c \) lower bounds the size of the smallest block of \( \pi_0 \). We will call such a translation as a \textit{C-uniform unit-translation} or simply a \textit{C-uniform translation}, where the context is clear. Before we proceed let us consider the following example.
Example 5.2. For $n = 8$, that is $\mathbb{Z}_n = \{0, 1, 2, 3, 4, 5, 6, 7\}$, let $z = 3$ and let $\pi_0 = \langle\{0, 5, 7\}, \{2, 3\}, \{1, 4, 6\}\rangle$. Here the block number for elements 0,5,7 is 0, that of 2,3 is 1 and that of 1,4,6 is 2. Let $\vec{C} = \langle\{5, 7\}, \{2, 3\}\rangle$, then for $\pi_0 \xrightarrow{\vec{c}} \pi_1$ we have $\pi_1 = \langle\{0\}, \{5, 7\}, \{2, 3, 1, 4, 6\}\rangle$. The block number for 0 is still 0, however the block number of 5,7 is now 1. This Translation causes the block number of the elements of $\mathbb{Z}_n$ to change. Notice that the number of elements entering and leaving each block is uniform ($c = 2 \leq |B_m^0|$) except the first and last blocks. Hence a $C$-uniform translation.

Lemma 5.6. There exists a $C$-uniform translation $\pi_0 \xrightarrow{\vec{c}} \pi_1$, if and only if no block $B_m^0$ ($0 \leq m < z - 1$) has $0 < c' < c$ elements, that is every block has $\geq c$ elements.

Proof. If every block has $\alpha c$ ($\alpha > 0$) elements then consider any $\vec{C} = \langle C_0, C_1, \cdots, C_{z-2}\rangle$ with $c_m = c$. Since $|B_m^0| \geq c$, $c_m \leq B_m^0$ is always true. On the contrary, if some block has $0 < |B_m^0| = c' < c$, then $c_m$ must be equal to $c$ for a $C$-uniform translation. However $c_m \leq |B_m^0| < c$. Thus the $C$-uniform translation is not possible.

Lemma 5.7. If $\pi_0 \xrightarrow{\vec{c}} \pi_1$ is a $C$-uniform translation with respect to $\vec{C}$, and if every block $B_m^0$ of ordered partition $\pi_0$ (for $0 \leq m < z - 1$) has $\geq c$ elements, then every block $B_m^1$ of ordered partition $\pi_1$ has equal number of elements, that is $|B_m^1| = |B_m^0|$.

Proof. Recall from Definition 5.1 that $B_m^1 = (B_m^0 - C_m) \cup C_{m-1}$ and $C_m \subseteq B_m^0$. Since $C_{m-1} \cap B_m^0 = \emptyset$, we have $|B_m^1| = |B_m^0| - |C_m| + |C_{m-1}| = |B_m^0| - c_m + c_{m-1}$. Now $c_m = c_{m-1} = c$, hence $|B_m^1| = |B_m^0| - c + c = |B_m^0|$.

Putting Lemmas 5.6, 5.7 together we can say that if the number of elements in every block (except possibly the last block) of $\pi_0$ is greater than or equal to $c$, then we can construct $C$-uniform translations

$$\pi_0 \xrightarrow{\vec{c}_0} \pi_1 \xrightarrow{\vec{c}_1} \pi_2 \cdots \pi_{k-1} \xrightarrow{\vec{c}_{k-1}} \pi_k$$

This is because the block size remains the same through the translations which is $\geq c$.

We now consider such a $C$-uniform $k$ translation in which from each block $B_m^i$, $c$ elements shift out to the corresponding block $B_{m+1}^{i+1} \in \pi_{i+1}$. 31
Example 5.3. For \( n = 16 \), that is \( \mathbb{Z}_n = \{0, 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15\} \), let 
\( z = 4 \) and let \( \pi_0 = \left( \begin{array}{c} 0 \end{array} \right) \). Let 
\( \vec{C}^0 = \langle \{9, 14\}, \{13, 15\}, \{6, 12\} \rangle \), then for \( \pi_0 \rightarrow \pi_1 \) we have 
\( \pi_1 = \left( \begin{array}{c} 0 \end{array} \right) \). Let \( \vec{C}^1 = \langle \{7, 8\}, \{2, 3\}, \{1, 4\} \rangle \), then for \( \pi_1 \rightarrow \pi_2 \) we have \( \pi_2 = \left( \begin{array}{c} 0 \end{array} \right) \). The block number for 0,5 is still 0, however the block number of 9,14 is now 1. These translations cause the block number of the elements of \( \mathbb{Z}_n \) to change. Notice that the number of elements entering and leaving each block is uniform (\( c = 2 \leq |B_m^0| \)), hence the block size remains constant (except the first and last blocks). Hence a \( C \)-uniform 2-translation. The extent of change in block numbers is tracked later in the chapter.

We recall from Example 5.3 that as we translate from one ordered partition to another, block number of elements of \( \mathbb{Z}_n \) change. We now track the extent of change to the block numbers as we progress through a \( C \)-uniform \( k \) translation \( \pi_0 \rightarrow \pi_1 \rightarrow \pi_2 \rightarrow \cdots \pi_k \). Specifically, we consider any element \( a \in B_m^0 \) whose block number in \( \pi_0 \) is \( m \). As we move from \( \pi_0 \) to \( \pi_1, \pi_2, \cdots \pi_k \), the block number of \( a \) could increase. This increase is calculated in following paragraphs.

As defined in Definition 5.2 we can say that there is a rank \( r \) of the elements of \( \mathbb{Z}_n \), that is constant with \( \pi_i \) (\( 0 \leq i \leq k \)), where \( \pi_i \) is produced from \( C \)-uniform \( k \) translation of \( \pi_0 \) through \( \pi_k \). Considering \( \pi_0 \rightarrow \pi_1 \), let some element \( a \in B_m^0 \) move to block \( B_{m+1}^1 \); recall that \( a \in B_m^1 \) or \( B_{m+1}^1 \). Thus the maximum increase in the block number of element \( a \) is 1. If \( g(k) \) denotes the maximum increase in the block number of an element as we translate from \( \pi_0 \) to \( \pi_k \), then \( g(1) = 1 \). Figure 5.2 shows the contents of a block \( B_{m+1}^1 \) with \( \geq c \) elements. Clearly \( a \in B_m^0 \) that moved to \( B_{m+1}^1 \) is one of the lowest ranked \( c \) elements of \( B_{m+1}^1 \). Let 
\[
\left| \frac{|B_{m+1}^1| - c}{c} \right| = \left| \frac{|B_{m+1}^1|}{c} \right| - 1 = \ell_1 \text{ (where } |B_{m+1}^1| \geq c \text{).}
\]
After another \( \ell_1 \) translation \( a \in B_{m+1}^1 \) moves to \( B_{m+2}^2 \) only if it is one of the largest \( c \) ranked elements of \( B_{m+1}^1 \). If \( \ell_1 \geq 1 \), then
$|B_{m+1}^1| \geq 2c$ and $a$ will not move out of $B_{m+1}^1$ to $B_{m+2}^2$. It will remain in $B_{m+1}^1$, that is block number of $a$ will not increase. In fact, it is easy to show that the block number of $a$ will increase only after $\ell_1$ additional translations. So for all $1 \leq \ell' \leq \ell_1$, $a \in B_{m+1}^{\ell'}$. It’s possible that $a \in B_{m+2}^{\ell'+1}$. Table 5.1 extends this argument to show the number of translations needed for each block number increase. We now restrict our attention to a particular case where

\[
\begin{array}{|c|c|}
\hline
\text{Block Number} & \text{Number of Translations} \\
\hline
m & 0(\text{initial}) \\
m + 1 & 1 = k_1 \\
m + 2 & k_1 + \left\lfloor \frac{|B_{m+1}^{k_1}|}{c} \right\rfloor = k_2 \\
m + 3 & k_2 + \left\lfloor \frac{|B_{m+2}^{k_2}|}{c} \right\rfloor = k_3 \\
\vdots & \vdots \\
m + h & k_{h-1} + \left\lfloor \frac{|B_{m+h-1}^{k_{h-1}}|}{c} \right\rfloor = k_h \\
\hline
\end{array}
\]

Table 5.1: Block Number of $a \in B_m^0$ over translation

$|B_0^0| \geq kc$. We call this a non-depleting $C$-uniform $k$-translation. It is easy to verify that each block $B_i^0 \geq 0$ ($0 \leq i < k$), since $|B_m^0| \geq c$ for all $0 < m < z - 1$, hence $B_m^1 \geq c$. This is because for $m > 0$, a block receives $c$ elements and gives up $c$ elements. It is only $B_0^1$ that does not receive any element for non-depleting $C$-uniform $k$-translation.

Hence:

\[|B_0^i| = |B_0^0| - ic \quad \text{for} \quad 0 \leq i < k \quad (5.8)\]

and

\[|B_m^i| = |B_m^0| \quad \text{for} \quad 0 < m < z - 1 \quad (5.9)\]
In Table 5.1 we see that \(k_h > k_{h-1} \geq 1\). Therefore using Equation 5.9, we can say \(|B^k_m| = |B^0_m|\). Thus from the table, we can say

\[
\begin{align*}
    k_h &= k_{h-1} + \left[ \frac{|B^{k-1}_{m+h-1}|}{c} \right] \\
    &= k_{h-1} + \left[ \frac{|B^0_{m-h+1}|}{c} \right] \\
    &= k_{h-1} + \left[ \frac{\ell_{m+h-1}}{c} \right] \\
    &= k_{h-2} + \left[ \frac{\ell_{m+h-2}}{c} \right] + \left[ \frac{\ell_{m+h-1}}{c} \right] \\
    &= k_{h-3} + \left[ \frac{\ell_{m+h-3}}{c} \right] + \left[ \frac{\ell_{m+h-2}}{c} \right] + \left[ \frac{\ell_{m+h-1}}{c} \right] \\
    &\vdots \\
    &= k_1 + \left[ \frac{\ell_{m+1}}{c} \right] + \left[ \frac{\ell_{m+2}}{c} \right] + \cdots + \left[ \frac{\ell_{m+h-1}}{c} \right] \\
    &= 1 + \sum_{i=1}^{h-1} \left[ \frac{\ell_{m+i}}{c} \right]
\end{align*}
\]

An unintended consequence of this is the following Lemma.

**Lemma 5.8.** If for every \(0 < m < z - 1\), \(|B^0_m| = b\) then \(k_h = 1 + (h - 1) \times \left\lfloor \frac{b}{c} \right\rfloor\) \(\square\)

Since \(g(k_h) = h\) we can have as many as \(1 + (h - 1) \times \left\lfloor \frac{b}{c} \right\rfloor\) non-depleting \(C\)-uniform \(k\)-translation and still have each element’s block number increase by at most \(h\).

Suppose we have an ordered partition \(\pi_0 = \langle B^0_m : 0 \leq m < z \rangle\) with \(z\) blocks in which \(B^0_0\) has at least \(k_{h,c}\) elements and each of \(B^0_1, B^0_2, \ldots, B^0_{z-1}\) has at least \(c\) elements each. Then \(\pi_0\) can be translated to \(\pi_h\) (according to a series of translations \(\pi_0 \xrightarrow{C^0} \pi_1 \xrightarrow{C^1} \pi_2 \cdots \pi_{k-1} \xrightarrow{C^{k-1}} \pi_k\)) ensuring that the block number of any element of \(\mathbb{Z}_n\) increases (corresponding to that of \(\pi_0\)) by at most \(h\).

Observe that since we are using a \(C\)-uniform translation with each \(\overrightarrow{C^i} = \langle C^i_0, C^i_1, \ldots, C^i_{2^n-2} \rangle\) has \(|C^i_m| = c\).

**Example 5.4.** For \(n = 16\), that is \(\mathbb{Z}_n = \{0, 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15\}\), let \(z = 4, c = 2\) and let \(\pi_0 = \langle \{11, 0, 5, 7, 8, 9, 14\}, \{2, 3, 13, 15\}, \{1, 4, 6, 12\}, \{10\} \rangle\).

Let \(\pi_0 \xrightarrow{\overrightarrow{C^0}} \pi_1\) we have
$\pi_1 = \left\{ \begin{array}{ll} \{11,0,5,7,8\}, & \text{block 0} \\
\{9,14,2,3\}, & \text{block 1} \\
\{13,15,1,4\}, & \text{block 2} \\
\{6,12,10\} & \text{block 3} \end{array} \right\}$. Let $\vec{C}^1 = \langle \{7,8\}, \{2,3\}, \{1,4\} \rangle$, then for $\pi_1 \xrightarrow{\vec{C}^1} \pi_2$ we have $\pi_2 = \left\{ \begin{array}{ll} \{11,0,5\}, & \text{block 0} \\
\{7,8,9,14\}, & \text{block 1} \\
\{2,3,13,15\}, & \text{block 2} \\
\{1,4,6,12,10\} & \text{block 3} \end{array} \right\}$. Let $\vec{C}^2 = \langle \{0,5\}, \{9,14\}, \{13,15\} \rangle$, then for $\pi_2 \xrightarrow{\vec{C}^2} \pi_3$ we have $\pi_3 = \left\{ \begin{array}{ll} \{11\}, & \text{block 0} \\
\{0,5,7,8\}, & \text{block 1} \\
\{9,14,2,3\} & \text{block 2} \\
\{13,15,1,4,6,12,10\} & \text{block 3} \end{array} \right\}$.

These 3 translations cause the block number of the elements of $\mathbb{Z}_n$ to change by up to 2. Notice that the size of the first block is greater than the product of the number of translations and shift size, moreover the number of elements entering and leaving each block is uniform ($c = 2 \leq |B^a_m|$), hence the block size remains constant (except the first and last blocks). Hence a non-depleting $C$-uniform 3-translation ($\pi_0 \xrightarrow{\vec{C}^0} \pi_1 \xrightarrow{\vec{C}^1} \pi_2 \xrightarrow{\vec{C}^2} \pi_3$).

The block number for elements $\{11,0,5,7,8,9,14,2,3,13,15,1,4,6,12,10\}$ changed from $0,0,0,0,0,1,1,1,1,1,2,2,2,2,2,3,3,3,3,3,3,3$ respectively. Hence the change in block number for the elements are $0,1,1,1,1,2,2,1,1,2,2,1,1,1,1,0$, we call this as the increment vector for $\pi_0 \rightarrow \pi_h$. In general the increment vector $\langle d_0, d_1, \cdots, d_{n-1} \rangle$ gives $d_a$, the increase in block number for element $a \in \mathbb{Z}_n$ with $0 \leq d_a \leq h$.

Recalling that an ordered partition $\pi = \langle B_0, B_1, \cdots, B_{z-1} \rangle$ is represented in the address generator module (see Figure 5.3) as a sequence $\langle e_0, e_1, \cdots, e_a, \cdots, e_{n-1} \rangle$, where for all $a \in \mathbb{Z}_n$, $e_a = m$ if and only if $a \in B_m$. Clearly $0 \leq e_a < z$. Let us call the sequence $\langle e_a : a \in \mathbb{Z}_n \rangle$ as the partition vector. Given any partition vector $\vec{e}^0 = \langle e^0_a : a \in \mathbb{Z}_n \rangle$ (corresponding to ordered partition $\pi_0$) and an increment vector $\vec{d} = \langle d_a : a \in \mathbb{Z}_n \rangle$ for $\pi_0 \rightarrow \pi_q$, where $q = k$. One can generate the partition vector $\vec{e}^q = \langle e^q_a : a \in \mathbb{Z}_n \rangle$ as $e^q_a = e^0_a + d_a$. Notice that $0 \leq d_a \leq h$. Figure 5.3 shows how $\pi_q$ can be generated from $\pi_0$.

In the method of Jordan and Vaidyanathan [16] the address generator stored the vector for every ordered partition, it needs to use. This required a $2^q \times n \log z$ LUT with gate cost $O(2^q n \log z)$. Now we only need to store the increment vector, each of size $n \log h$ (as opposed to $n \log z$ for the entire ordered partition). If $2^q$ increment vectors are stored then
the cost is $O(2^y n \log h)$ for the LUT, $2n \log z$ for the input and output partition vectors. The additions even if performed by a ripple carry adder have $O(n \log z)$ cost. Thus the total cost is $O(n \log z + 2^y n \log h)$. If we set $2^y \log h = 2^y \log z$ then $y_1 = y \log \left( \frac{\log z}{\log h} \right)$. Now if $h$ is a constant then $y_1 = y \log \log z$. Thus with the same cost of $O(2^y n \log z)$, the enhanced MU-Decoder can now produce $2^y$ ordered partitions and $2^{y_1+x}$ subsets rather than the $2^{y+x}$ subsets in the original solution. Further generalizing this, if we use $2^y$ ordered partitions each capable of handling $2^{y_1}$ increment vectors, then with a cost of $O(2^y n \log z + 2^{y_1} n \log h)$ one could produce $2^{y+y_1+x}$ subsets. If $2^y \log z + 2^{y_1} \log h = O(2^y \log z)$ then we need $2^y = O \left( \frac{2^{y_1} \log h}{\log z} \right)$. This gives the hardware structure of Figure 5.4.

In general it is more efficient to decrease $y$ (to even 0), however, a larger $y$ allows for independent ordered partition generation. We will explore the $x = 0$ case further in Chapter 6.
Figure 5.4: Selector Module Hardware Structure
Chapter 6
Generic Subsets

Until now we have looked at generating a given totally-ordered set. In this chapter, we generate a generic set of subsets of \( \mathbb{Z}_n \), from which other subsets can be discovered.

6.1 Traversing the Boolean Lattice

Given a subset \( S \subseteq \mathbb{Z}_n \), supersets of \( S \) can be generated by adding one element of \( \mathbb{Z}_n - S \) at a time to \( S \). This amounts to moving up the Boolean lattice, one level at a time. In fact, a one-hot decoder generates subsets this way.

**Example 6.1.** On a 1-hot decoder the subset \( S_2 = \{0, 1, 2\} \) may be generated as \( S_0 = \{0\}, S_1 = S_0 \cup \{1\} \) and \( S_2 = S_1 \cup \{2\} \).

Similarly, subsets can be formed by removing one element at a time, that is moving down the Boolean lattice. Thus the path length of moving from subsets \( \hat{S} = (S_1 - S_2) \cup S_3 \) (where \( S_1 \cap S_2, S_1 \cap S_3 \) are empty) is \( |S_2| + |S_3| \) which is a good reflection of the cost of generating \( \hat{S} \) from \( S_1 \).

The overall idea is to generate \( S_1 \) in \( O(\log n) \) time and then spend another \( O(d \log n) \) time to generate \( S \) from \( S_1 \). The set \( S_1 \) is called a primary set and \( S \) (derived from \( S_1 \)) a secondary set. Now given a set \( \mathcal{S} = \{S_i : 0 \leq i < u\} \) of primary sets (produced by an MU-Decoder), we try to produce a set \( \hat{\mathcal{S}} = \{\hat{S}_i\} \supseteq \mathcal{S} \) of secondary subsets that can be produced within a distance \( d \) of \( \mathcal{S} \).

6.1.1 Single Total Order \( \mathcal{S} \)

Let \( \mathcal{S} = \{S_i : 0 \leq i < u\} \) be totally-ordered. Using Lemma 4.1, \( \mathcal{S} \) can be produced using an MU-Decoder in \( O(\log n) \) time. Select \( \mathcal{S} \) so that \( S_i \subseteq S_{i+1} \) and \( |S_{i+1}| - |S_i| \geq 2d \) This implies that the totally-ordered set \( \mathcal{S} \) occupies a path in the Boolean lattice in which individual elements of \( \mathcal{S} \) are at least \( 2d \) apart (see Figure 6.1). For each primary set \( S_i \) reaching in \( O(\log n) \) time using an MU-Decoder, secondary set at distance \( d \) can be reached. Since Hamming distance between any \( S_i \) and \( S_j \geq 2d \), a \( d \) distance set can be reached from
only one primary set.

Figure 6.1: 2\(d\) spaced totally-ordered subsets

Lemma 6.1. The number of \(d\)-distance secondary sets reachable from any primary set \(S_i \subseteq n\) is \(\binom{n}{d}\).

Proof. Out of the \(n\) possible elements in (or not in) \(S_i\), select \(d\) to be not in (or in) \(\hat{S}_i\) (a secondary set).

Example 6.2. For set \(\{0,1,2\}\), a 1-hot decoder generates \(\{0\}\), then generates \(\{1\}\) and accumulates this with \(\{0\}\) to get \(\{0,1\}\) and finally generates \(\{3\}\) to accumulate with prior result to get \(\{0,1,2\}\).

In general, the above accumulation is a move up from a point in the Boolean Lattice. A move down can be done in a similar way (using a active 0 1-hot decoder).

Given any set \(S\), another set \(S'\) that differs from \(S\) in \(d\) elements (Hamming distance \(d\)) can be produced from \(S\) in \(d\) iteration. Here we create a set of primary subsets \(\hat{S}\), such that each pair of elements of \(\hat{S}\) has a Hamming distance \(> 2d\), see Figure 6.1.
Lemma 6.2. If primary set of subsets is totally ordered and if it has a pairwise Hamming distance of at least $2d$, then $\binom{n}{a}z^2 \log z$ subsets can be generated in $O(d \log n)$ time by an MD($([\log(z - 1)], 0, z, n)$).

Proof. This MU-Decoder represents a single line with $2^x = z$ elements. Therefore each source word corresponds to one primary subset and there is only one partition being used ($y = 0$). The Totally-ordered for a single line is proved by Jordan and Vaidyanathan [17] and each subset in the totally-ordered line can be used to reach $\binom{n}{a}$ subsets (Lemma 6.1)

The following theorem is deduced in a similar way to Lemma 6.1 using Theorem 4.1 (page 21).

Theorem 6.1. Now if we have $2^y \leq \frac{n}{2^d}$ ($d \geq 1$) primary sets, where all the subsets have a pairwise Hamming distance $\geq 2d$, then $\binom{n}{a}z^2 \log 2^y$ totally-ordered subsets can be generated in $O(d \log n)$ time by an MD($([\log(z - 1)], [\log(z \log z)], z, n)$).

Proof. $2^y \leq \frac{n}{2^d}$ primary subsets can be spaced as Figure 6.1.

To see the significance of this result, we observe that for $d = 1$ we have $nz^2 \log z$ subsets at $O(nz \log z)$ cost and $O(\log n)$ delay. However with $z = 2$, there is no significant change to the delay and cost, but the number of subsets produces increases to approximately $\frac{n^2}{2}nz^2 \log z$ (an increase by an $\frac{n^2}{2}$ factor). Given that $n$ is much larger than $z$, this is a substantial increase in the number of subsets produced. More generally, for about the same cost and delay, with any constant $d$ we will have a polynomial increase in the number of subsets produced.

Let $S = S_0 \cup S_1 \cup \cdots S_{Y-1}$, where the $S_i$’s are pairwise disjoint and equal in size. Suppose $S_i = L \circ \pi_i$. Then $S$ can be generated as shown in Definition 2.4. Now we look into the fact whether all these $\pi_i$ ordered partitions can be generated from a single ordered partition $\pi_0$ as shown in Lemma 5.8 (see page 34). For us to translate $k$ ordered partitions from ordered partition $\pi_0$, we need $|B_0^k| > kc$ and each block in $\pi_0$ to have at least $c$ elements (except the last one). We pick any $S$ (path of $XY$ elements) where $X = |L|$ the source set size and
$Y$ is the number of partitions, including the primary and the translated ones. Let elements of $S_i$ be subsets uniformly distributed on the path. That is, $|S_i - S_{i-1}| \geq c \geq 2d$ for each $i$ (see Figure 6.2). Now let us divide the $XY$ totally-ordered subsets from Figure 6.2 into $Y$ sets of subsets as below

$$S_0 = \{S_{Y-1}, S_{2Y-1}, \ldots, S_{iY-1}, \ldots, S_{XY-1}\}$$
$$S_1 = \{S_{Y-2}, S_{2Y-2}, \ldots, S_{iY-2}, \ldots, S_{XY-2}\}$$
$$\vdots$$
$$S_j = \{S_{Y-(j+1)}, S_{2Y-(j+1)}, \ldots, S_{iY-(j+1)}, \ldots, S_{XY-(j+1)}\}$$
$$\vdots$$
$$S_{Y-1} = \{S_0, S_Y, \ldots, S_{(i-1)Y}, \ldots, S_{(X-1)Y}\}$$

Now $S_j = \{S^j_0, S^j_1, \ldots, S^j_i, \ldots, S^j_{X-1}\}$, where $S^j_i = S_{(i+1)Y-(j+1)} = L_i \circ \pi_j$ and $S^j_0 = S_{Y-(j+1)}$.

For $\pi_j = \langle B^j_0, B^j_1, \ldots, B^j_{X-1} \rangle$, we know $B^j_0 = S^j_0, B^j_i = S^j_i - S^j_{i-1}$ and $B^j_X = \mathbb{Z}_n - S^j_{X-1}$. We used $X = 2^x = z - 1$ from Jordan and Vaidyanathan [16].

**Lemma 6.3.** Every block of ordered partition $\pi_j$ is non-empty, except block $B^j_X$ which is possibly empty.

**Proof.** $B^0_i - B^j_i = S^0_i - S^j_i = S_{Y-1} - S_{Y-(j+1)}$. Since $Y - 1 - (Y - (j+1)) = j$, we can say $|S_{Y-1}| - |S_{Y-(j+1)}| = cj$. Clearly $S^0_0 \supset S^j_0$ so is $B^0_0 \supset B^j_0$.

In general,

$$B^0_i - B^j_i = (S^0_i - S^0_{i-1}) - (S^j_i - S^j_{i-1})$$
$$= (S_{(i+1)Y-1} - S_{(i)Y-1}) - (S_{(i+1)Y-(j+1)} - S_{(i)Y-(j+1)})$$

Now $$(i + 1)Y - 1 \geq (i + 1)Y - (j + 1) \geq iY - 1 \geq iY - (j + 1)$$
or $$(S_{(i+1)Y-1} \supset (S_{(i+1)Y-(j+1)} \supset S_{(i)Y-1}) \supset S_{(i)Y-(j+1)})$$

Hence $B^0_i - B^j_i = S_{(i+1)Y-1} - S_{(i+1)Y-(j+1)}$
or $|B^0_i - B^j_i| = jc$
Hence we can say $\pi_0 \to \pi_j$ is a non-depleting $C$-uniform $k$-translation. We can say that if element $a$ moves into $B_i^j$ from $B_{i-1}^j$, then $a$ never moves out of $B_i^j$. Hence $S^0 = S_0, S_1, \ldots, S_{Y-1}$ can be generated from a single ordered partition $\pi_0$. Therefore for each totally-ordered set, the number of generator partitions is only 1 which is $\pi_0$ and the rest are generated using the increment vector. Hence $y = y_1 + y_2 = y_2$.

**Lemma 6.4.** $S_0, S_1, \ldots, S_{Y-1}$ are disjoint.

**Proof.** We can say from the construction of Figure 6.1 that since $|S_i - S_{i-1}| \geq c$, hence each output subset is at least $c$ distance apart from the nearest subset and hence disjoint.
Figure 6.2: $XY$ Totally-Ordered subsets, each circle has radius $d$ from the primary subset
Chapter 7
Conclusion

7.1 Work Covered

In this thesis, we proved that for any source set and any output set we can produce an ordered partition which will map the given source set to the output, as long as the source set and output set are totally-ordered. We further expand on this to say that for a given set of output sets, which are individually totally-ordered, they can be produced from any given single totally-ordered source set we produce the required set of ordered partitions for this output set (for the same source set). This alleviates the limitations due to isomorphism (see Jordan and Vaidyanathan [16]). This allows us to produce different sets of individually totally-ordered output sets from a single totally-ordered source set, hence effectively reducing the cost of using a large LUT-Decoder. A set of subsets as represented in Figure 1.3(b) (see page 5), can be produced with the same cost as a regular MU-Decoder, but with the isomorphism constraint, the cost would rise significantly.

We further worked on hardware enhancements for generating additional ordered partitions from existing ordered partitions, inside the MU-Decoder itself, leading to an increase in the number of subsets produced for about the same hardware cost. We enhance the existing hardware of the MU-Decoder to produce these additional subsets. This additional hardware doesn’t affect the complexity of previously assumed cost of the MU-Decoder and generates $\Theta(\log z)$ additional subsets. Producing these additional subsets has certain limitations, the generator and generated subsets together are totally-ordered. So far we generated, for $\pi_0$, the ordered partitions $\pi_1, \pi_2, \cdots, \pi_k$. The subsets $S_i = L \circ \pi_i$ are totally-ordered. In fact we assert that $\bigcup_i S_i$ is totally-ordered. However, our method does not require this. All we impose is that $S_0 \cup S_i$ must be totally-ordered (as in Figure 7.1). That is, while we have proved our method for $\bigcup_i S_i$ being totally-ordered, we have strong evidence that is all we require for $S_0 \cup S_i$ to be totally-ordered. In this thesis, we focused our work producing subsets which
can be produced cheaply, and using these subsets to approach a given set of subsets at a distance of up to \( \log n \) from the produced subset. In this thesis, we focused our work producing the “best” sets of subsets. Together with the 1-hot Decoder, we generate \( \binom{n}{d} 2^y z^2 \log z \) subsets in \( O(d \log n) \) time using a MU-Decoder \( \text{MD}([\log(z - 1)], [\log(z \log z)], z, n) \) of gate cost \( O(zn \log z) \) and delay \( O(\log n) \) where \( 2^y \leq \frac{n}{2^d} \) and \( (d \geq 1) \). This is a substantial expansion of the MU-Decoder range for efficient operation.

### 7.2 Future Work

This work opens up several possible directions for future work.

Is the translation algorithm the best possible? Are similar algorithms (to generate ordered partition) possible for non-totally-ordered sets?

The cost of enhancement is driven by the cost \( O(n2^y) \) of the increment vector. We assume that these vectors are independent. Could one be derived from the other?

The MU-Decoder itself is a reconfigurable device being reconfigured through its LUTs. Can the MU-Decoder be used as a LUT? For example the increment vector \( \pi \) as big as a subset of \( \mathbb{Z}_n \).

So far the MU-Decoder used is “universal” [17] in which all \( z \) source bits are sent to all \( n \) muxes. We want to figure if there is a “good” set of subsets (as in Chapter 6) for which each MUX receives only a subset of source bits. This would drastically reduce the cost of the interconnects in the mapping unit and the address selector LUT size. (It has been shown by Kongari [21] that these interconnect is a large contributor to the MU-Decoder area.)

Can multiple MU-Decoder be used to produce different ranges of subsets of \( \mathbb{Z}_n \)?
Figure 7.1: Multiple Totally-Ordered paths produced from one Totally-Ordered set of subsets
Bibliography


Vita

Utsav Agarwal was born on March 3, 1989, in Kolkata, India. He received his Elementary education from Abhinav Bharati High School, following which he received the rest of his schooling from The Assembly of God Church School. He joined Meghnad Saha Institute of Technology in the field of Electronics and Communication Engineering and graduated as a Bachelor of Technology in May 2012. After working for a couple of years in India, he joined Louisina State University, USA as a graduate student in Electrical and Computer Engineering Department. He is expected to receive his Master of Science in Electrical Engineering in May 2017.